L Number Hits Search Text DB Time star	02 13:56 02 13:56
2 2979206 writ\$3 with predetermined logical value\$1	02 13:56
2 2979206 writ\$3 with predetermined logical value\$1 USPAT;	
2 2979206 writ\$3 with predetermined logical value\$1 DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT; ISPAT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	
2979206 writ\$3 with predetermined logical value\$1 USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT; US-PG	
3 567617 (memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1) 4 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	
3 567617 (memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1) 4 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	02 14:00
3 567617 (memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1) 4 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JP	02 14:00
3 567617 (memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1) 4 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	02 14:00
3 567617 (memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1) USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IMM_TDB USPAT; US-PGPUB; EPO; JPO; IMM_TDB USPAT; US-PGPUB; IMM_TDB USPAT; US-PGPUB; IMM_TDB USPAT; US-PGPU	02 14:00
predetermined logical value\$1) 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 Sample Sepo; IPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; IPO; DERWENT; IBM_TDB USPAT; US-PGPUB; ISPO; IPO; DERWENT; IBM_TDB USPAT; US-PGPUB; IBM_TDB USPAT	UZ 14:00
4 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; I	
reading out near1 writtern logical value\$1 near2 memory cell\$1 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	
reading out near1 writtern logical value\$1 near2 memory cell\$1 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	
reading out near1 writtern logical value\$1 near2 memory cell\$1 1967002 reading out near1 writtern logical value\$1 near2 memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGP	
5 338229 ((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) 6 1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) 7 199841 (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1) uS-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; Deswent; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	02 1A·01
5 338229 ((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1) and (writ\$3 with predetermined logical value\$1) and (reading out near1 writtern logical value\$1) 6 1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) 7 199841 (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1) usPAT; UsPGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; USPGPUB; EPO; JPO; (writtern logical value\$1)) and (compar\$3 and (writ\$3 with predetermined logical value\$1)) and (compar\$3 and (writ\$1 with read-out logical value\$1)) DERWENT; IBM_TDB USPAT; UsPGPUB; EPO; JPO; JPO; DERWENT; IBM_TDB USPAT; UsPGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	02 17:01
5 338229 ((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) 6 1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) 7 199841 (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1) (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1)) 8 2366221 discontinu\$3 near1 test with failure memory cell\$1 USPAT; USPGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; USPGPUB; EPO; JPO; IBM_TDB USPAT; USPGPUB; EPO;	
((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) 1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) ((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1) ((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) and (compar\$3 and (writtern logical value\$1)) and (compar\$3 and (writtern logical value\$1)) and (compar\$3 and (writtern logical value\$1)) and (compar\$3 and (because the product logical value\$1)) and (compar\$3 and (compar\$3 and logical value\$1)) and (compar\$3 and logical value\$1)) and (compar\$3 and logical value\$1)) and (compar\$3 and logical value\$1) and (compar\$3 and logical value\$1)) and (compar\$3 and logical value\$1) and (compar\$3 and logical value\$3	
((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1) predetermined logical value\$1) and (reading out near1 writtern logical value\$1) predetermined logical value\$1 with read-out logical uspan; logical value\$1) 1017682 compar\$3 and (writtern logical value\$1 with read-out logical uspan; logical value\$1) (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1) logical value\$1 mear2 memory cell\$1) and (compar\$3 and (writtern logical value\$1)) predetermined logical value\$1)) logical value\$1)) ((writtern logical value\$1 with read-out logical value\$1)) discontinu\$3 near1 test with failure memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO;	
predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) 1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) 199841 (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1) and (compar\$3 and (writtern logical value\$1)) 8 2366221 discontinu\$3 near1 test with failure memory cell\$1 US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO;	02 14:02
logical value\$1 near2 memory cell\$1) 1017682 Compar\$3 and (writtern logical value\$1 with read-out logical value\$1) USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; (writtern logical value\$1 with read-out logical value\$1)) DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; USPAT; US-PGPUB; USPAT	
6 1017682 compar\$3 and (writtern logical value\$1 with read-out logical VSPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB 7 199841 (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1)) 8 2366221 discontinu\$3 near1 test with failure memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DEPO; JPO; USPAT; US-PGPUB; EPO; JPO; DEPO; JPO; DEPO; JPO; DEPO; JPO; DEPO; JPO; DEPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	
1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) 1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) 1017682 compar\$3 and (writtern logical value\$1)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 with read-out logical value\$1) 1017682 compar\$3 and (writtern logical value\$1)) and (writ\$3 with predetermined logical value\$1)) and (compar\$3 and (writtern logical value\$1)) 1017682 compar\$3 and (writtern logical value\$1)) and (writ\$3 with predetermined logical value\$1)) and (compar\$3 and (writ\$1)) berwell the product of t	
6 1017682 compar\$3 and (writtern logical value\$1 with read-out logical value\$1) USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; Derwent; Iogical value\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1)) DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO;	
value\$1) Value\$1) Value\$1) US-PGPUB; EPO; JPO; DERWENT; IBM_TDB (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1) and (compar\$3 and (writ\$1)) (writtern logical value\$1 with read-out logical value\$1)) WS-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO;	02 14:02
7 199841 (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writ\$1)) EPO; JPO; (writtern logical value\$1 with read-out logical value\$1)) 8 2366221 discontinu\$3 near1 test with failure memory cell\$1 USPAT; USPAT; USPAT; USPADB; EPO; JPO; EPO; JPO;	
7	
7 199841 (((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writ\$4 with read-out logical value\$1)) 8 2366221 discontinu\$3 near1 test with failure memory cell\$1 USPAT; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO;	
predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1)) 8 2366221 discontinu\$3 near1 test with failure memory cell\$1 US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	
logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1)) 8 2366221 discontinu\$3 near1 test with failure memory cell\$1 USPAT; US-PGPUB; EPO; JPO;	02 14:03
(writtern logical value\$1 with read-out logical value\$1)) DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	
8 2366221 discontinu\$3 near1 test with failure memory cell\$1 USPAT; US-PGPUB; EPO; JPO;	
8 2366221 discontinu\$3 near1 test with failure memory cell\$1 USPAT; US-PGPUB; EPO; JPO;	
US-PGPUB; EPO; JPO;	NO 14·N7
EPO; JPO;	UZ 17.U/
DERWENT;	
IBM_TDB	
9 199841 ((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with USPAT; 2004/02/	02 14:08
predetermined logical value\$1)) and (reading out near1 writtern US-PGPUB;	•
logical value\$1 near2 memory cell\$1)) and (compar\$3 and EPO; JPO;	
(writtern logical value\$1 with read-out logical value\$1))) and DERWENT;	
(discontinu\$3 near1 test with failure memory cell\$1) IBM_TDB	
10 274 repair\$3 and (memory with impossible) USPAT; 2004/02/	14 ⋅08
US-PGPUB;	1.00
EPO; JPO;	J. 11.00
DERWENT;	J. 11.00
IBM_TDB	
US-PGPUB;	02 14:08
EPO; JPO; DERWENT;	
IBM_TDB	
. - 	
US-PGPUB;	02 14:08
EPO; JPO;	
DERWENT;	02 14:08
IBM_TDB	02 14:08

والميرو					
	13	1975440	(uncorrectable error\$) or (uncorrectable memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:09
	14	274	(repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:10
	15	156	((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and ((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:11
	16	4375786	substitut\$3 with spare memory component\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:11
	17	156	(((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and ((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:12
	18	5887707	number near1 failure memory cell\$1 with reach\$2 predetermined number	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:13
	19	156	((((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1)) and (number near1 failure memory cell\$1 with reach\$2 predetermined number)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:17
	20	153032	erased en bloc	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:18
	21	35	(((((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1)) and (number near1 failure memory cell\$1 with reach\$2 predetermined number)) and (erased en bloc)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:19
	22	4378	714/718.ccls. or 714/719.ccls. or 714/710.ccls.or 714/704.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:20

Marita.				
23	7	(714/718.ccls. or 714/719.ccls. or 714/710.ccls.or 714/704.ccls.) and (((((((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1)) and (number near1 failure memory cell\$1 with reach\$2 predetermined number)) and (erased en bloc))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:21